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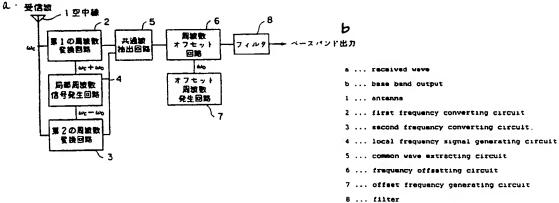
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(54) Tide: RECEIVING CIRCUIT

### (54) 発明の名称 受信回路



### (57) Abstract

A receiving circuit for digital demodulation communication system including channels, which reduces the A receiving circuit for digital demodulation communication system including channels, which reduces the power consumption of the receiving system, has a simple structure and consumes a small power. Two frequencies higher and lower the middle value of the frequencies of the channels are supplied to first and second frequency converting circuit (2 and 3) respectively from a local frequencyt signal generating circuit (4). Two output signals are generated for each of three signals of desired wave, higher channel, and lower channel. The desired wave commonly existing in the circuits (2 and 3) are extracted by means of a common wave extracting circuit (5). The frequency offset wo in the output of the circuit (5) is removed by means of a frequency offset circuit (6) and the unnecessary frequency components are removed through a filter (8). By using a commons wave extracting circuit (5) having a transformer to utilize the mutual inductance, the difference between the common and noncommon waves is twice larger than that to utilize the mutual inductance, the difference between the common and noncommon waves is twice larger than that of conventional.